



Dr. P K Meher founded **Sandhaan Labs** that acts as a platform for developing synergy between academia and enterprises to drive innovation and develop the necessary technology solutions by high-quality research through global collaboration. He envisions **Sandhaan Labs to be a global center for pioneering research** to solve societal problems and catering to industry requirements through a vibrant and cohesive network of researchers around the world.

He has the unique experience of teaching and research in Physics, Electronics, and Computer Engineering for more than 35 years. He started his career as a Lecturer in Physics and worked as a Reader in Electronics in Berhampur University (1993-1997), as a Professor of Computer Applications in Utkal University (1997-2002), and as a Professor of Computer & System Sciences, Visva-Bharati, Shantiniketan for a short span thereafter. He has worked in Nanyang Technological University (NTU), Singapore in senior research positions during 2002 to 2016. Currently, he is involved in research consultancy service with several reputed organisations.

He has demonstrated various models of collaborative research and provided expert research mentoring to several doctoral and post-doctoral candidates. Dr. Meher has published **more than 250 research papers** in top-tier international journals and conferences which includes **nearly 90 papers in IEEE Transactions, mostly through collaborative research.** Recently, he has co-edited a book, 'Arithmetic Circuits for DSP Applications' which is published by the IEEE Press. He is within top 5% of the researchers world-wide in ResearchGate Score. He was conferred with the **Samanta Chandrasekhar Award for Research in Engineering & Technology**, for the year 1999. Some of his other recognitions include the Best paper award of the Springer Journal of Circuits, Systems and Signal Processing for the year 2013 and the Sydney R. Parker Best Paper Award for the year 2016. He has a h-index of 42 and an i-10 index of 133 on Google Scholar and has nearly 6000 citations.

During the last 15 years, Dr. Meher has served as **Associate Editor for various IEEE Transactions.** He is currently serving as Associate Editor for IEEE Transactions on Circuits & Systems: Regular Papers and the Springer Journal of Circuits, Systems, and Signal Processing. He has served as a speaker of **Distinguished Lecturer Program of IEEE Circuits & Systems Society** and has delivered talks in India and several other countries. Dr. Meher is a Life Fellow of the Institution of Electronics and Telecommunication Engineers, India and a Senior Member of the Institute of Electrical and Electronic Engineers (IEEE) from 2003. He was elected as a Fellow of the Institution of Electrical Engineers (IEE), UK, in 2004. He was registered as a Chartered Engineer in the Engineering Council, UK in the year 2005. More details about his work and publications are available at the following links.

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